



T2081/T1042-based Single Board Computer

High Performance/Low Power DO-254 Certifiable SBC IP

Features and Benefits

- Part of the COTS-D family of safety certifiable modules
- Rugged conduction-cooled module - 3U VITA 65 (OpenVPX™) solution
- A complete hardware solution with data package to support certification to RTCA DO-254/EUROCAE ED-80
- Board Support Packages (BSP) available, as well as data packages to support certification to RTCA DO-178C/EUROCAE ED-12C
- The provision of all IP and data to enable manufacture, support, and repair of the module

Introduction

The T2081/T1042-based Single Board Computer (SBC) is ideal for avionics and defense applications requiring the best performance per watt from a safety certifiable Power Architecture®-based processor including an XMC expansion site to maximize the functionality in a single-slot of a 3U VPX system.

The T2081/T1042 SBC design comes with all the data needed for support, manufacturing and repair of the module, as well as data to support DO-254/ED-80 hardware certification. The SBC also comes with off-the-shelf certifiable BSPs for most certifiable O/S's, complete with data to support DO-178C/ED-12C software certification. The available data kits support both FAA and EASA certification authority needs.

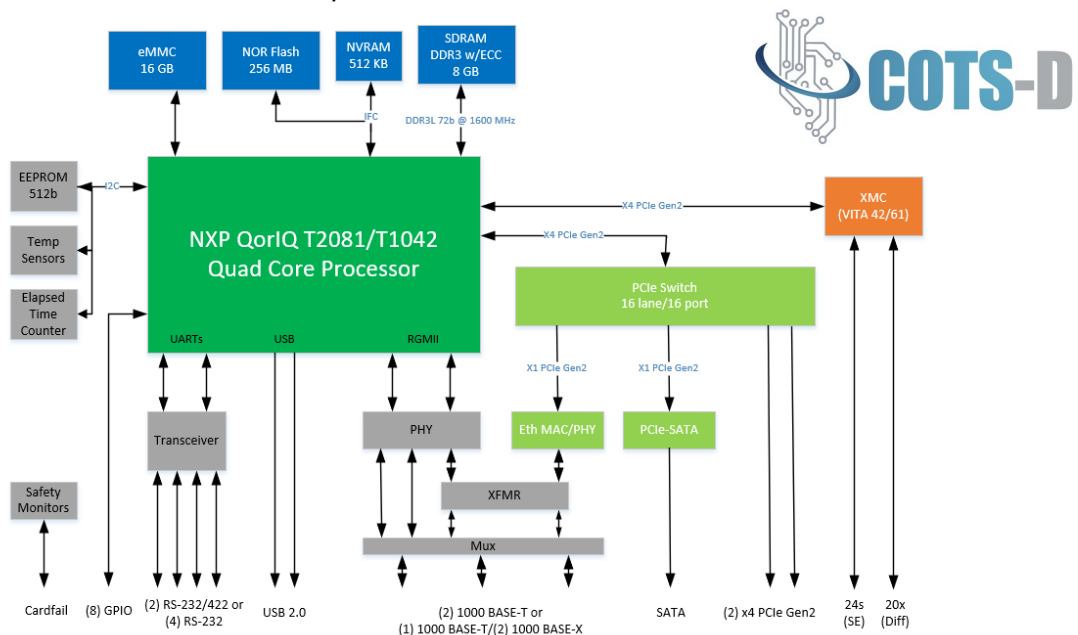


Figure 1: T2081/T1042 SBC Block Diagram



Processing

The Central Processing Unit (CPU) is the NXP® T2081/T1042 Power Architecture processor from the QorIQ® Advanced Multiprocessing (AMP) series. The T2081/T1042 embedded processor was architected to provide maximum performance per watt, and enable small form factor conduction-cooled solutions with high performance and functionality. The advanced features include four e6500/e5500 64-bit Power Architecture cores operating at maximum clock along with low latency, large caches, advanced interconnect and memory hierarchy.

NXP QorIQ AMP processors are ideal for the aerospace and defense market, providing integrated features such as ECC, parity, support for hardware assisted virtualization and partitioning enforcement, along with safety and security blocks that make them suitable for robust applications. In addition, these processors are manufactured in large volume, providing confidence for “in-service experience”.

Memory

The following table summarizes the available memory:

Memory	Size	Interface	Typical Use
SDRAM – DDR3	8 GB	DDR3L 72b @ 1600 MHz	Main application memory
NOR Flash	256 MB	LPC	Application program storage
NVRAM	512 KB	LPC	Application non-volatile data storage
EEPROM	512B	I2C	Product information (product identification, serial number, MAC address etc.)
eMMC	16 GB	eMMC	Mass storage for digital map data, application database information etc.

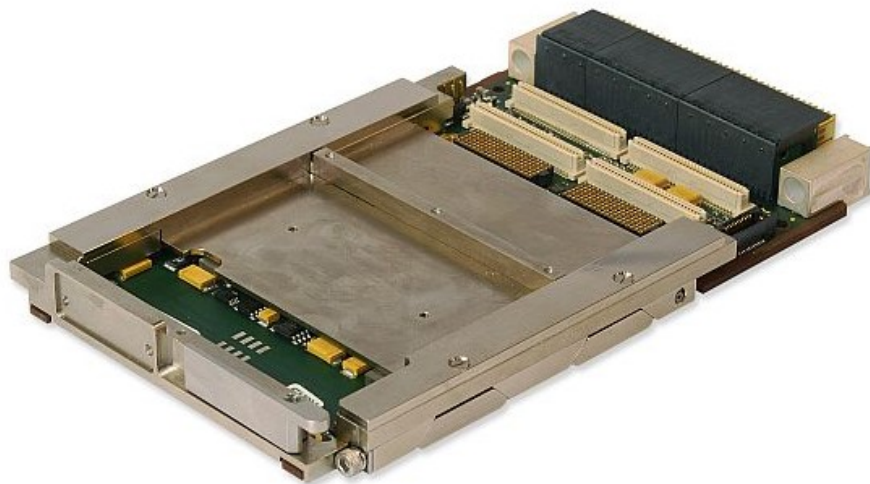


Figure 2: T2081/T1042-based COTS-D SBC



Inputs and Outputs

The following table summarizes the available input and output interfaces.

Interface	Quantity	Typical Use
RS-232/RS-422	4	<p>Asynchronous communication with one transmit and one receive signal for development and maintenance.</p> <p>The option is available to select between 2 and 4 serial port interfaces with transmit and receive functions.</p> <ul style="list-style-type: none"> When configured for two serial ports, each serial interface is software configurable as either RS-232 or RS-422. If the configuration is for two ports and is in RS-232 mode, hardware handshaking is supported (CTS/RTS). When configured for four serial ports, all four serial interfaces are RS-232. Baud rates are independently configurable for each serial port and support standard baud rates from 300 to 115200 Baud.
Ethernet 10/100/1000 BASE-T (2) or 1000 BASE-T (1) + 1000 Base-X (2) (certifiable UDP/IP interface)	3	High speed data transfer with other systems, sensors and central storage
LVTTTL Discrete I/O	8	Single-ended discrete signal monitoring and driving (5V tolerant)
I2C	1	Interface to custom functions on the backplane or system level I/O panel
PCIe x4 Gen 2	2	High speed bus interconnect to communicate with other modules within a system
SATA 2.0	1	Interface to recorder, removable storage to provide mission data and recording
USB 2.0	2	Adding storage or peripherals for use by a non-certifiable application partition



Local Functions

Local functions include an Elapsed Time Counter (ETC) to track module up-time to assist with maintenance activities.

The T2081/T1042 provides two four channel DMA engines that are available for general purpose use, which may be used for transferring data between the SDRAM, Flash memory, device bus peripherals, Gigabit Ethernet and the PCIe bus. The DMA controllers have bandwidth management to control the distribution of bandwidth between the DMA channels.

Expansion

Within a 3U VPX system, the capabilities of the module can be expanded. The processor may be configured to have two x4 PCIe Gen 2 communication/data links with up to two independent modules without the need for an additional PCIe switch.

Local card expansion is supported by a standard XMC 2.0 (VITA 61) expansion interface with a 4-lane PCIe Gen 2 interface. The Pn6 I/O is mapped per VITA 46.9 P2W1-X24S+X8D+X12D (24 single ended and 20 differential I/O interfaces) to the backplane. VITA 61 is based on the VITA 42 XMC replacing the connectors with alternative footprint-compatible ruggedized high speed mezzanine interconnect. VITA 61 is pin compatible with the VITA 42 standard, and therefore it supports VITA 42 as a build option. The XMC site supports industry standard XMC modules including the CoreAVI E9171 COTS-D Graphics Controller which is available to provide high performance graphics capability.

Safety and Security Features

In addition, the module provides a suite of safety monitors to support safety requirements. These include temperature, voltage and clock. The temperature monitors, two for monitoring module temperature and one for monitoring the T2081/T1042 die temperature, are under application control while voltage and clock out of range would result in the module being shut down. A failure of any of the monitor, or a software failure indicator, results in the assertion of a Card Fail indicator to the backplane. In addition to the monitors, there is an independent watch dog timer that will time out and cause a module reset if not “kicked” by the application before timing out.

Software

RTOS Board Support

DO-178C/ED-12C certifiable Board Support Packages (BSP) are available off-the-shelf for the following operating systems. Please contact CoreAVI for other RTOS BSP support. In addition to the Board Support Package, CoreAVI is able to provide a complete DO-178C/ED-12C certifiable software package including the Real Time Operating System and its artifacts, certifiable Built-In-Test (BIT) Software, certifiable graphics drivers – all pre-integrated and ready to go. As the single provider of the entire software suite, support is easy to obtain and CoreAVI’s expertise in software certification ensures the lowest risk.

- Wind River® VxWorks® 653
- DDC-I Deos™
- Lynx Software LynxOS®-178



Firmware

The T2081/T1042 SBC includes boot loader firmware to perform basic initialization of the module prior to loading and starting the application. Two versions of the boot loader are provided, a development boot loader with which you can develop, load and debug the application, and a DO-178C/ED-12C certifiable boot loader, included with the RTOS BSP, which is needed to start the application once it's developed.

Application Development Support

The T2081/T1042 COP probe signals are made available to the backplane and Rear Transition Module (RTM) where they are provided in a standard Test Access Port (TAP) to assist with application development and debug.

Rear Transition Module (RTM)

For development purposes a RTM is available that plugs into the backside of the backplane in the same slot as the T2081/T1042 SBC. The RTM provides access to the T2081/T1042 SBC I/O through commercial industry standard connectors, headers and cables.

Lifecycle

The T2081/T1042 is part of NXP's Product Longevity program which ensures stable supply for 15 years from product launch (October 2012) making it one of the longest available Power Architecture processors available today.

Specifications

The T2081/T1042 SBC is designed to run from the +5V rail (VS3) with maximum typical current per the following table based on the execution of a representative application at the highest rated operating temperature.

Voltage	Typical Current (Amps)
+12V	Only if required by XMC module
-12V	Only if required by XMC module
+5V	11/7
+12V Auxiliary	Not used
-12V Auxiliary	Not used
+3.3V Auxiliary	0.1



The T2081/T1042 SBC dimensions are per VITA 48.2 for a 0.8” pitch conduction-cooled module, without solder-side cover. With 2-level maintenance (2LM) covers, the module conforms to the VITA 48.2 standard and has an 0.85” pitch conduction-cooled. The weight is less than 800 grams.

The environmental specifications are per the following table:

Environment	Specification
Operating Temperature	-40 to +85°C at module edge
Storage Temperature	-55 to +105°C
Vibration	0.1 g2/Hz (max), 5 to 2000 Hz
Shock	40 g. 11ms sawtooth
Humidity	0% to 95% non-condensing (dependent on conformal coating used)

The T2081/T1042 SBC is compatible with the following VITA 65 slot profiles:

- MOD3-PAY-2F2T-16.2.5-3
- MOD3-PAY-2F2U-16.2.3-3

The T2081/T1042 SBC can also be offered with a SOSA (Sensor Open Systems Architecture)-compatible pinout.

COTS-D Data Kit

The COTS-D Data Kit provides all the data to enable support, manufacturing and repair of the T2081/T1042 SBC. Key elements of the data package include:

- Manufacturing data
- Theory of Operations
- Bill of Materials including full manufacturers part numbers
- Schematics
- PLD design files
- Mechanical drawings and assembly diagrams
- Printed Circuit Board (PCB) drawings and data
- Qualification data

Please request the COTS-D application note which provides further details on the data items and support provided for the technical transfer. The technical transfer also includes training and support as you introduce the product to your manufacturing facility or that of your chosen contract manufacturer.



DO-254/ED-80 Data Kit

The DO-254/ED-80 data kit provides documentation and evidence for the T2081/T1042 to support FAA and EASA certification needs of the hardware elements, as well as PLDs to support Item Development Assurance Level (IDAL) A.

DO-178C/ED-12C Data Kit

The DO-178C/ED-12C data kit provides documentation and evidence to support FAA and EASA certification needs of the BSP.

Other Data

Failure Modes and Effects Analysis (FMEA), as well as a safety manual and other documentation to support ARP4761 system safety assessments are also available.

Ordering

The following T2081/T1042 SBC products can be ordered from CoreAVI:

- T2081/T1042 SBC module for development purposes
- T2081/T1042 SBC RTM for development purposes
- COTS-D Design/Manufacturing Data Kit & license to use
- Safety Critical Data Kit supporting DO-254/ED-80
- BSP
- DO-178C/ED-12C Level A Data Kit for the BSP
- Failure Modes and Effects Analysis (FMEA)
- Safety Manual

Contact CoreAVI to discuss your requirements and obtain a quote.